

FIG. 1 is a block diagram of a system 100. The system 100 includes a user computer 20, a central processing computer 10, a financial institution computer 50, a provider computer 30, a shipper computer 40, and a third party shipper computer 60. The user computer 20 is connected to the central processing computer 10. The central processing computer 10 is connected to the financial institution computer 50, the provider computer 30, the shipper computer 40, and the third party shipper computer 60. The financial institution computer 50 is connected to the provider computer 30. The provider computer 30 is connected to the shipper computer 40. The shipper computer 40 is connected to the third party shipper computer 60.

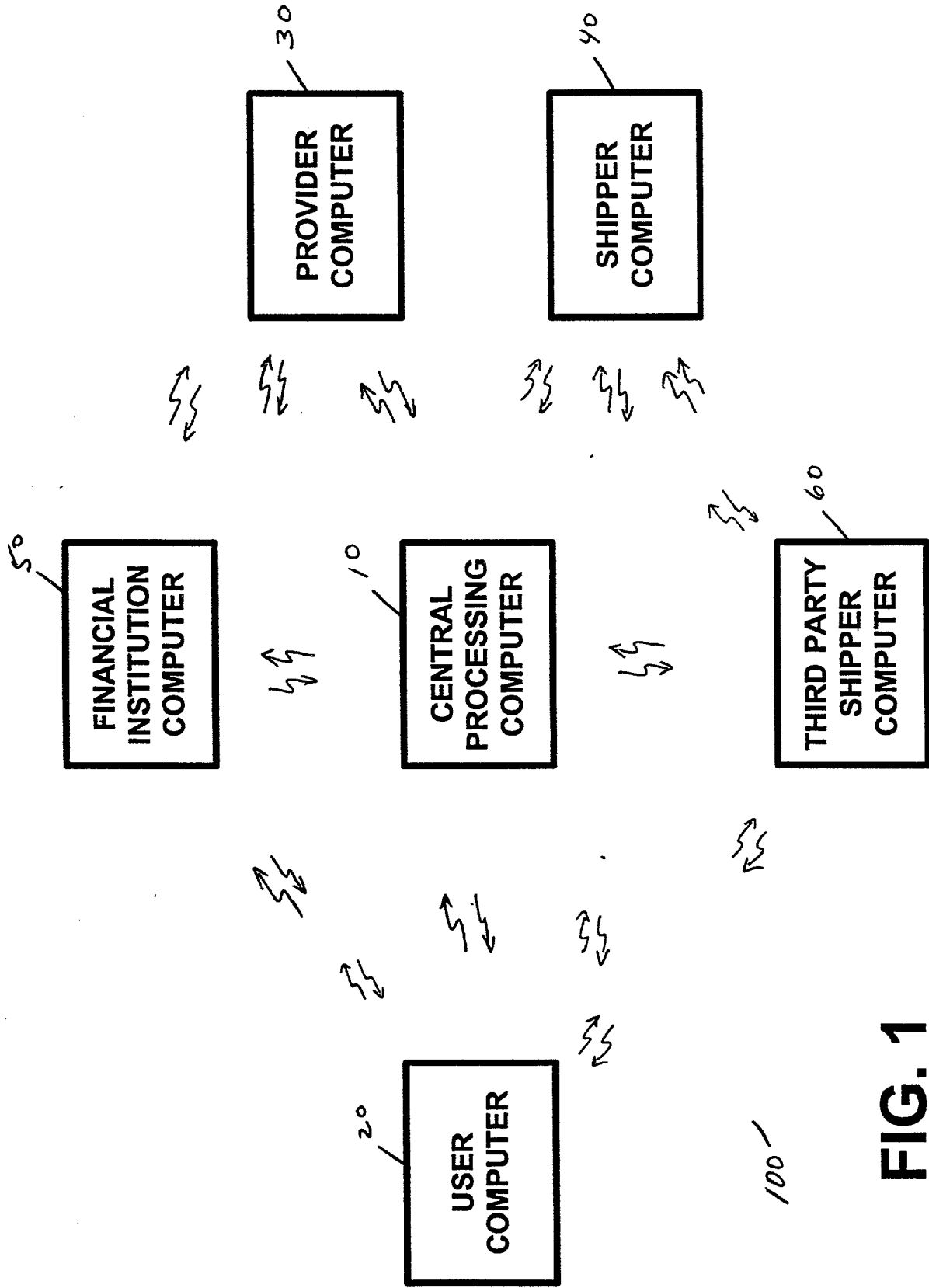
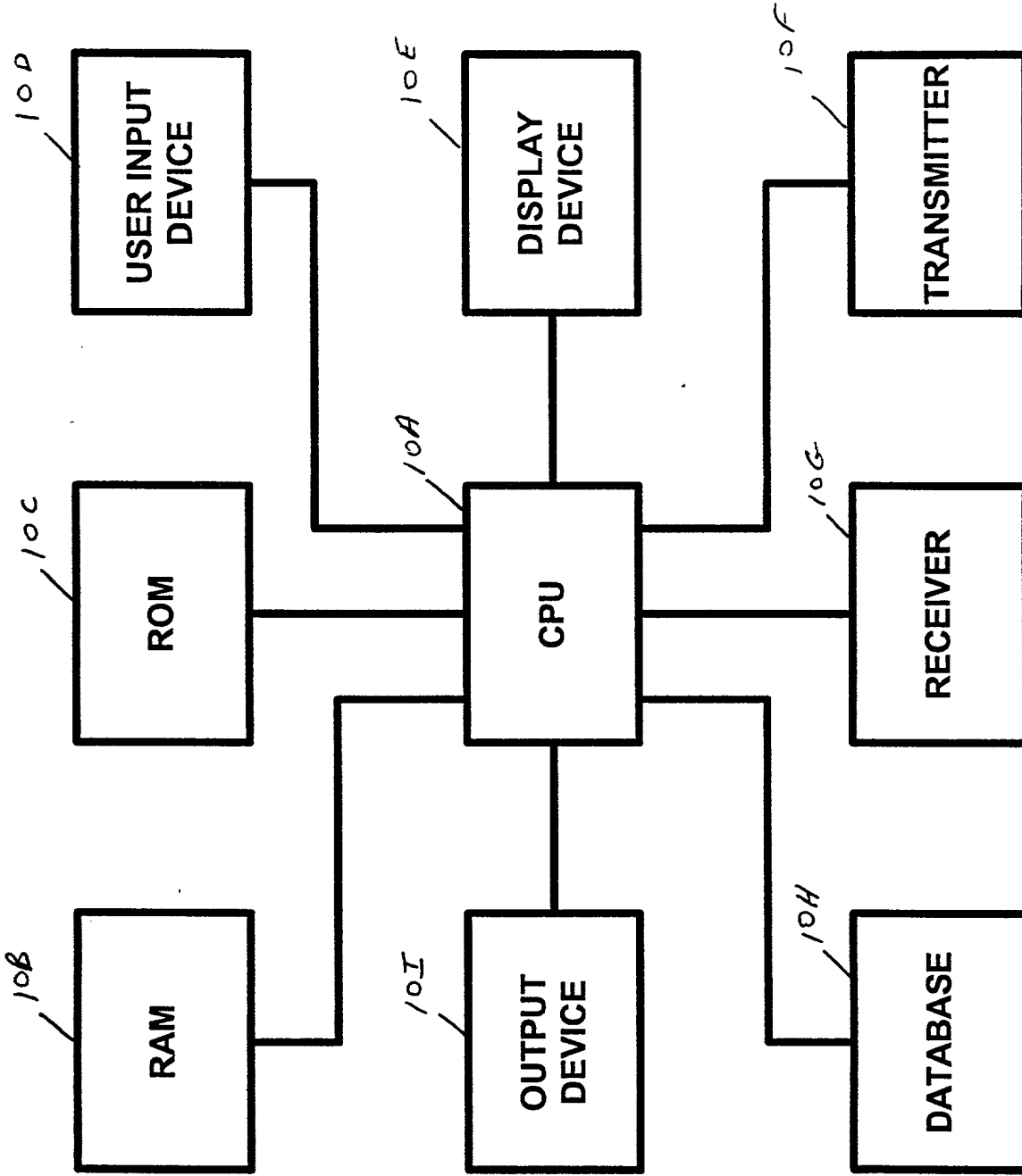
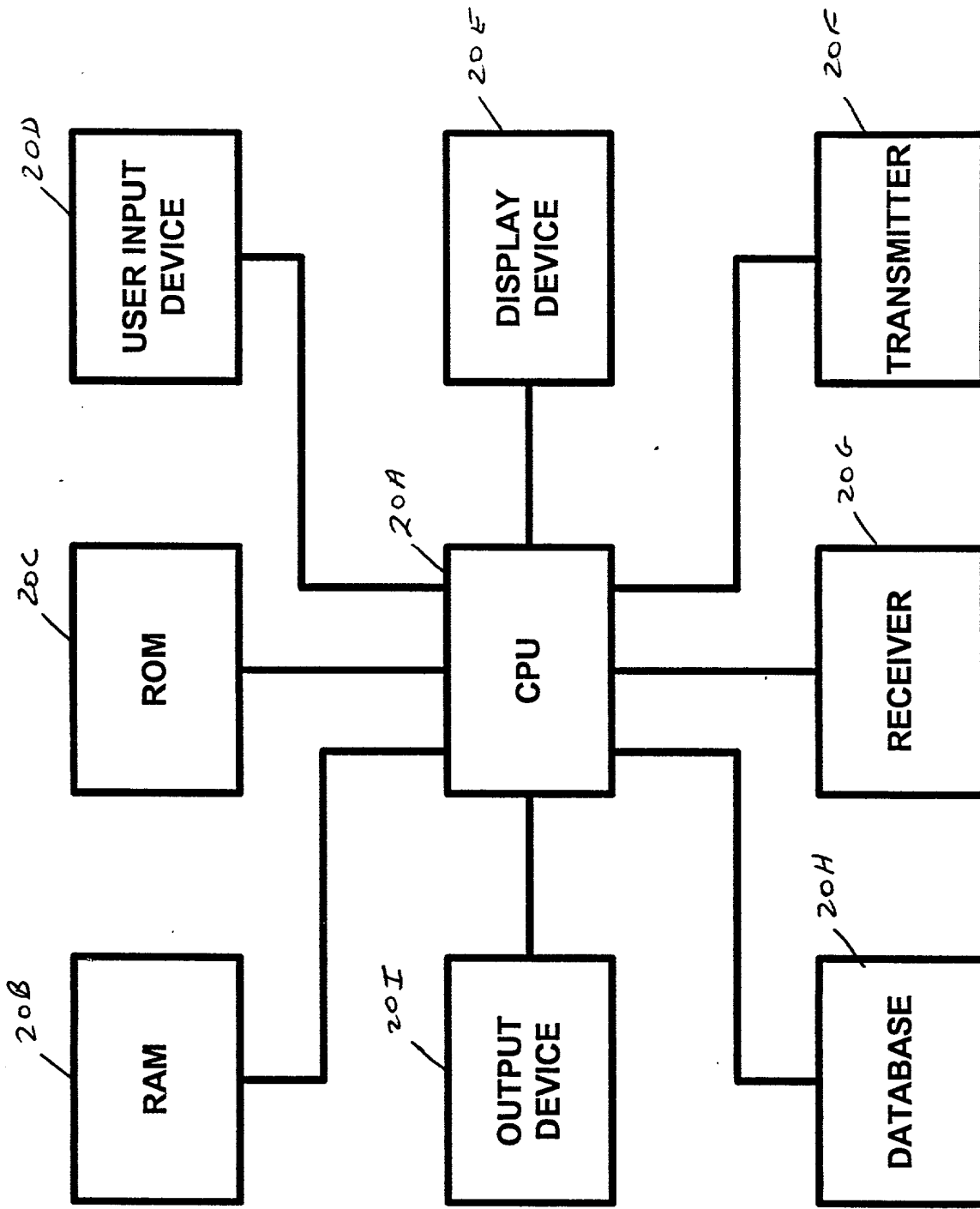


FIG. 1



100

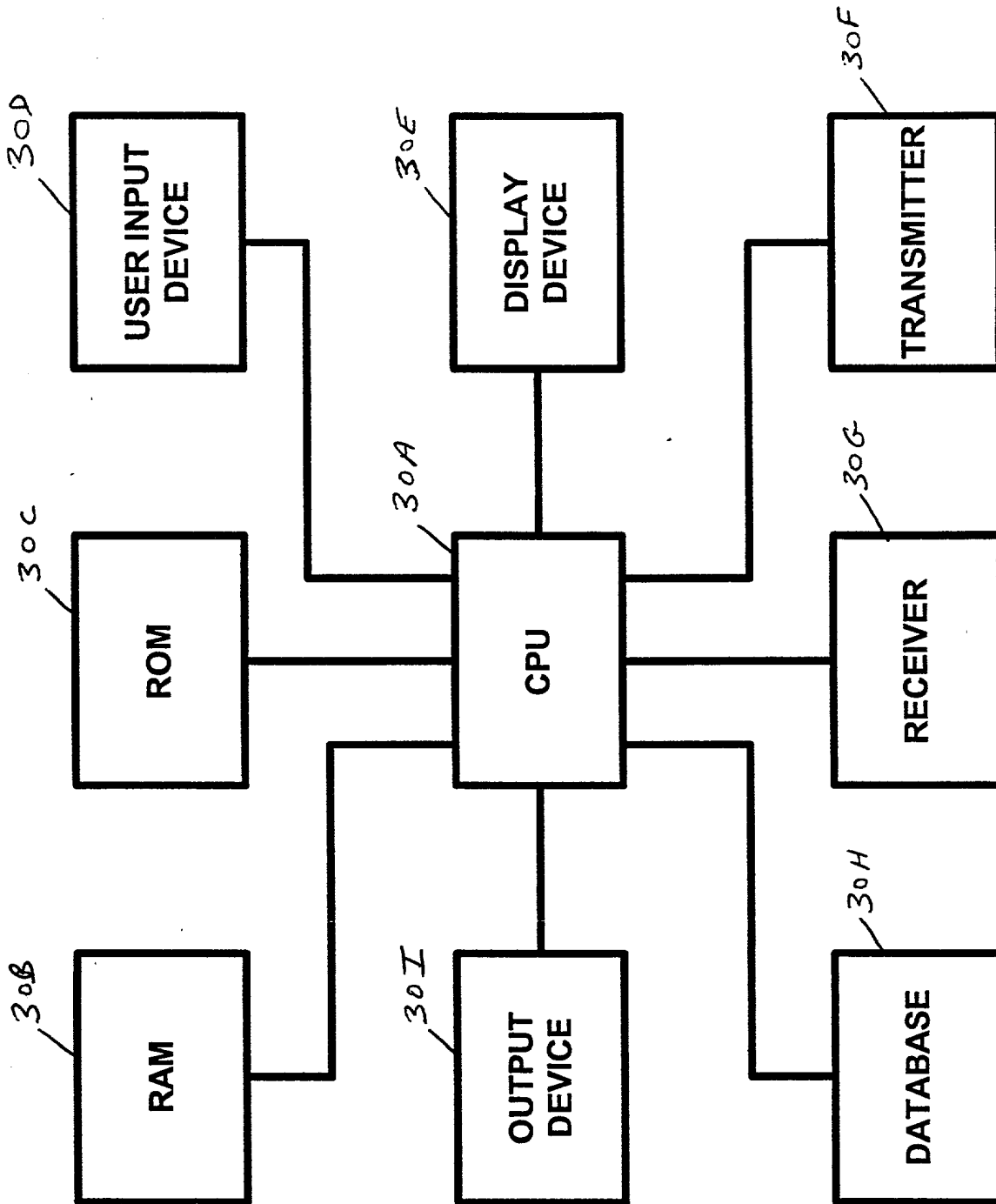
FIG. 2



20-

FIG. 3

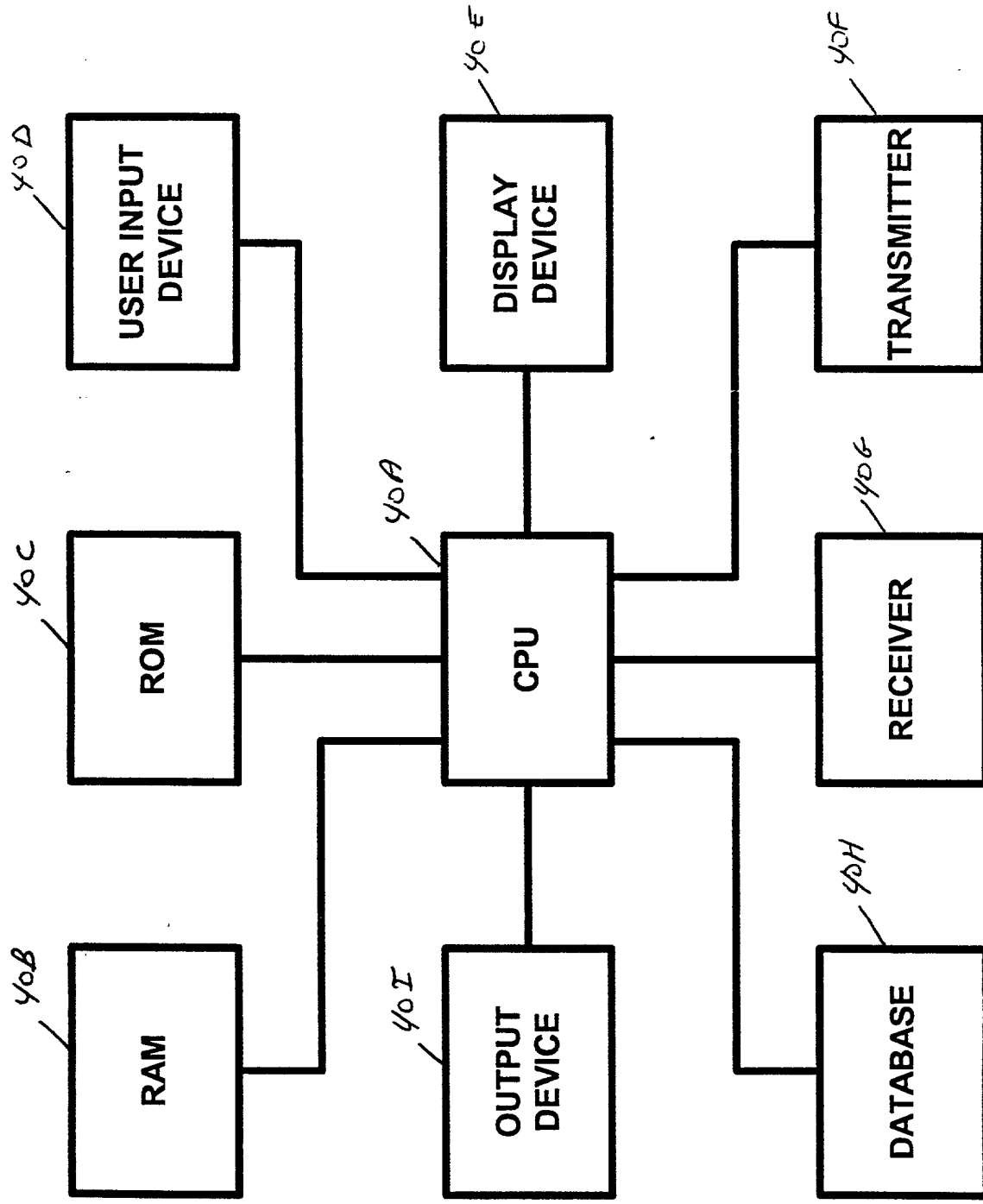
FIG. 4 is a block diagram of a computer system 300. The system 300 includes a central processing unit (CPU) 30A, a random access memory (RAM) 30B, a read only memory (ROM) 30C, a user input device 30D, a display device 30E, an output device 30I, a database 30H, a receiver 30G, and a transmitter 30F. The CPU 30A is connected to the RAM 30B, the ROM 30C, the user input device 30D, the display device 30E, the output device 30I, the database 30H, the receiver 30G, and the transmitter 30F.



30-

FIG. 4

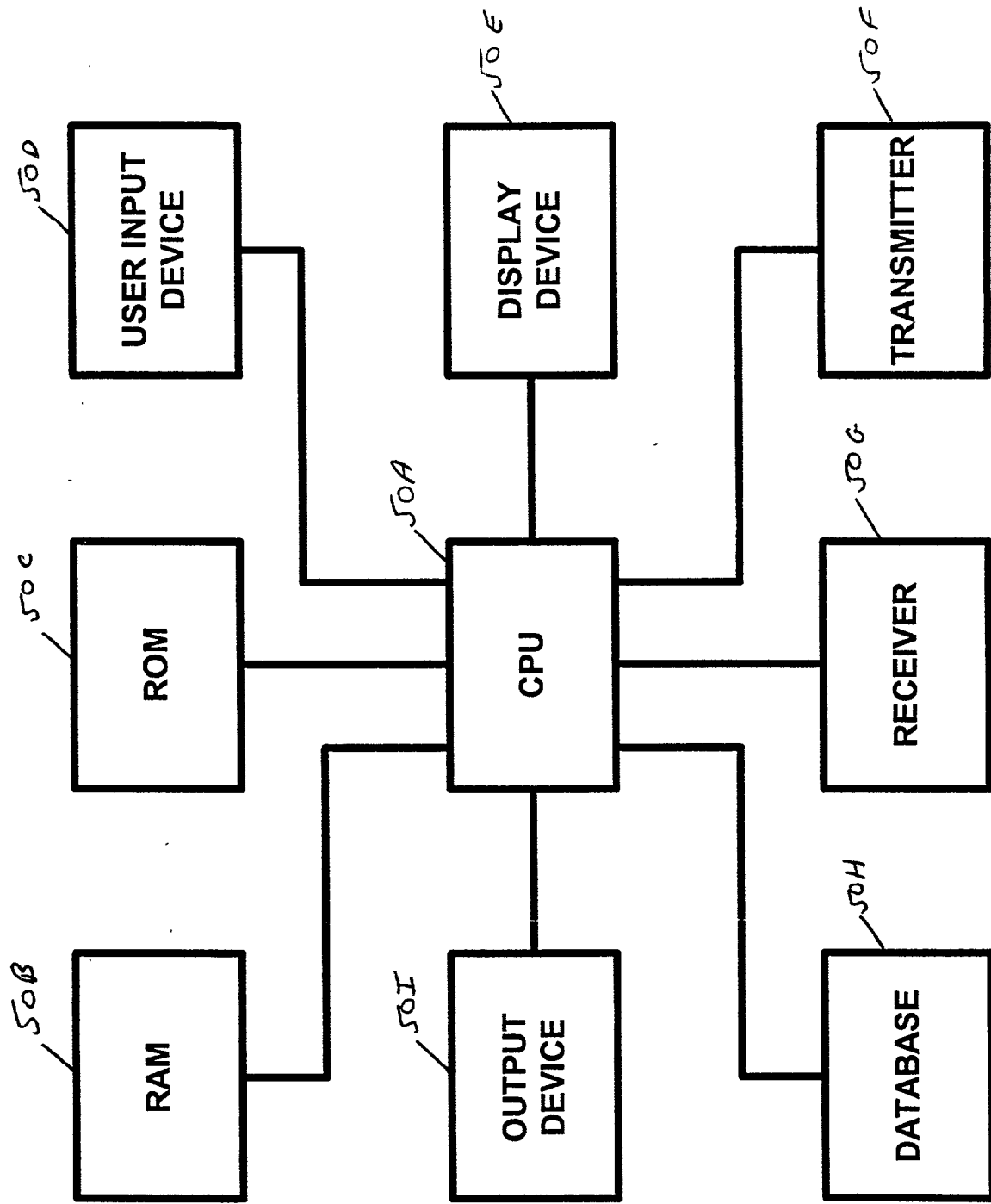
FIG. 5 is a block diagram of a system 400.



40-

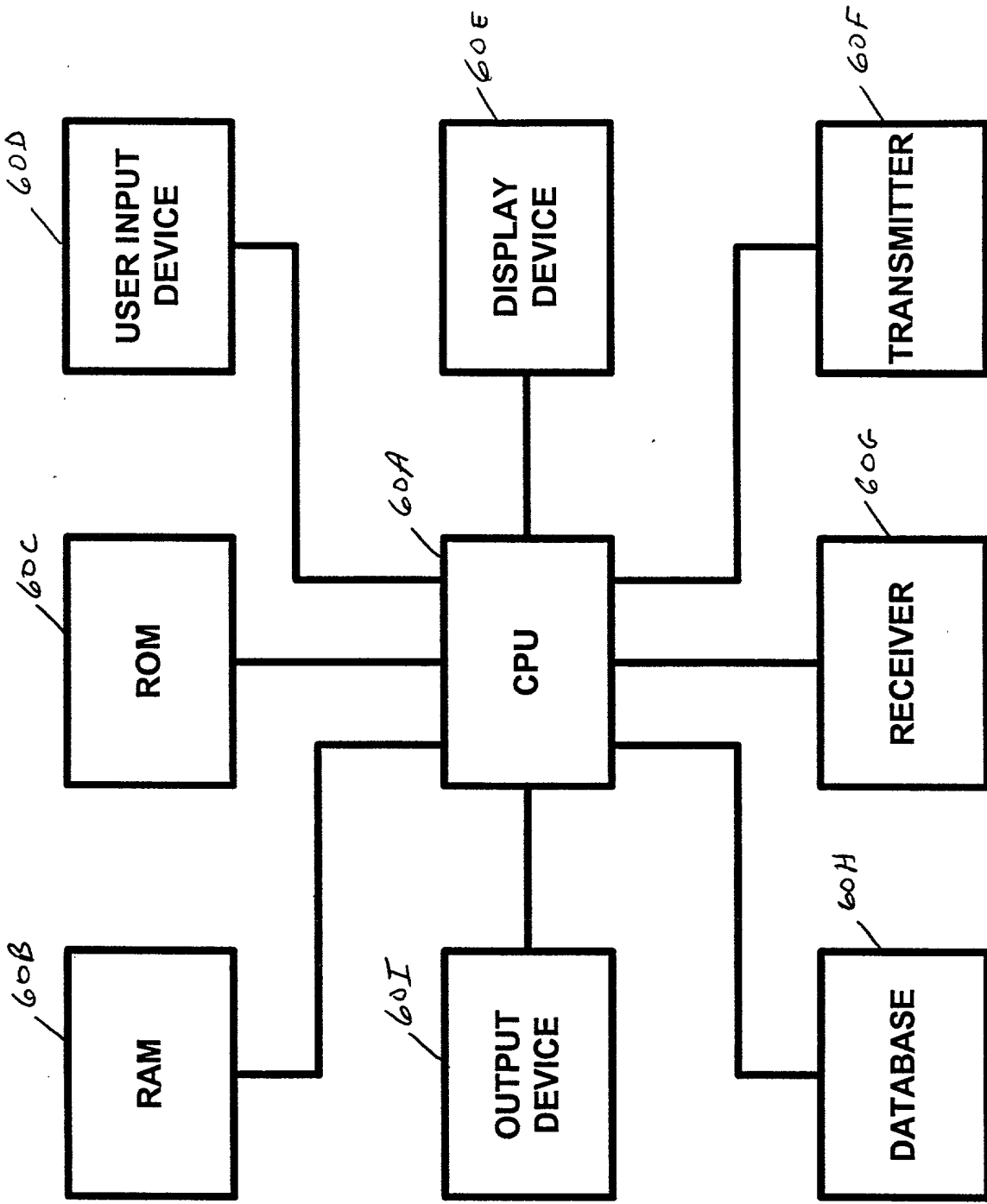
FIG. 5

FIG. 6 is a block diagram of a system 500.



50-

FIG. 6



600

FIG. 7

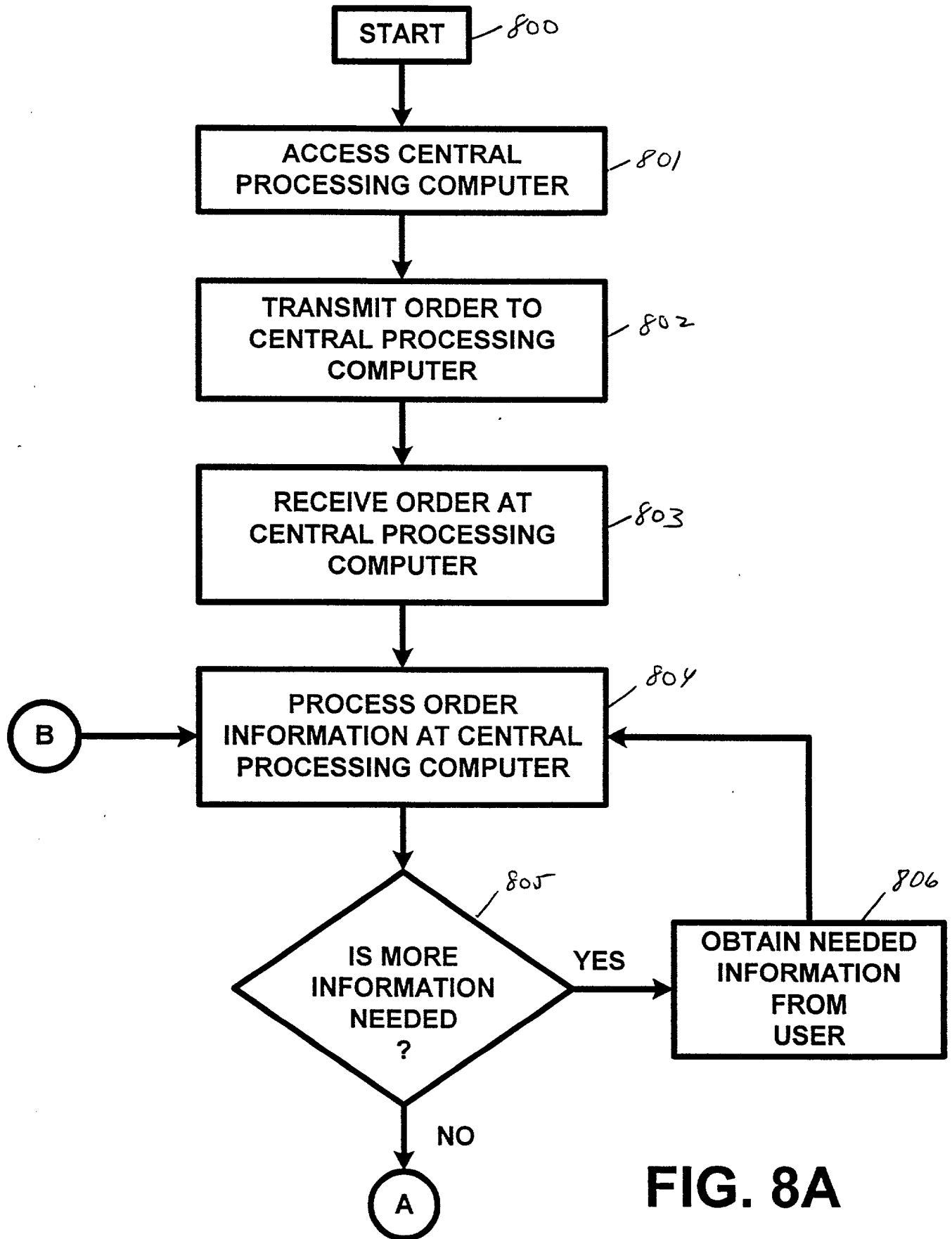


FIG. 8A

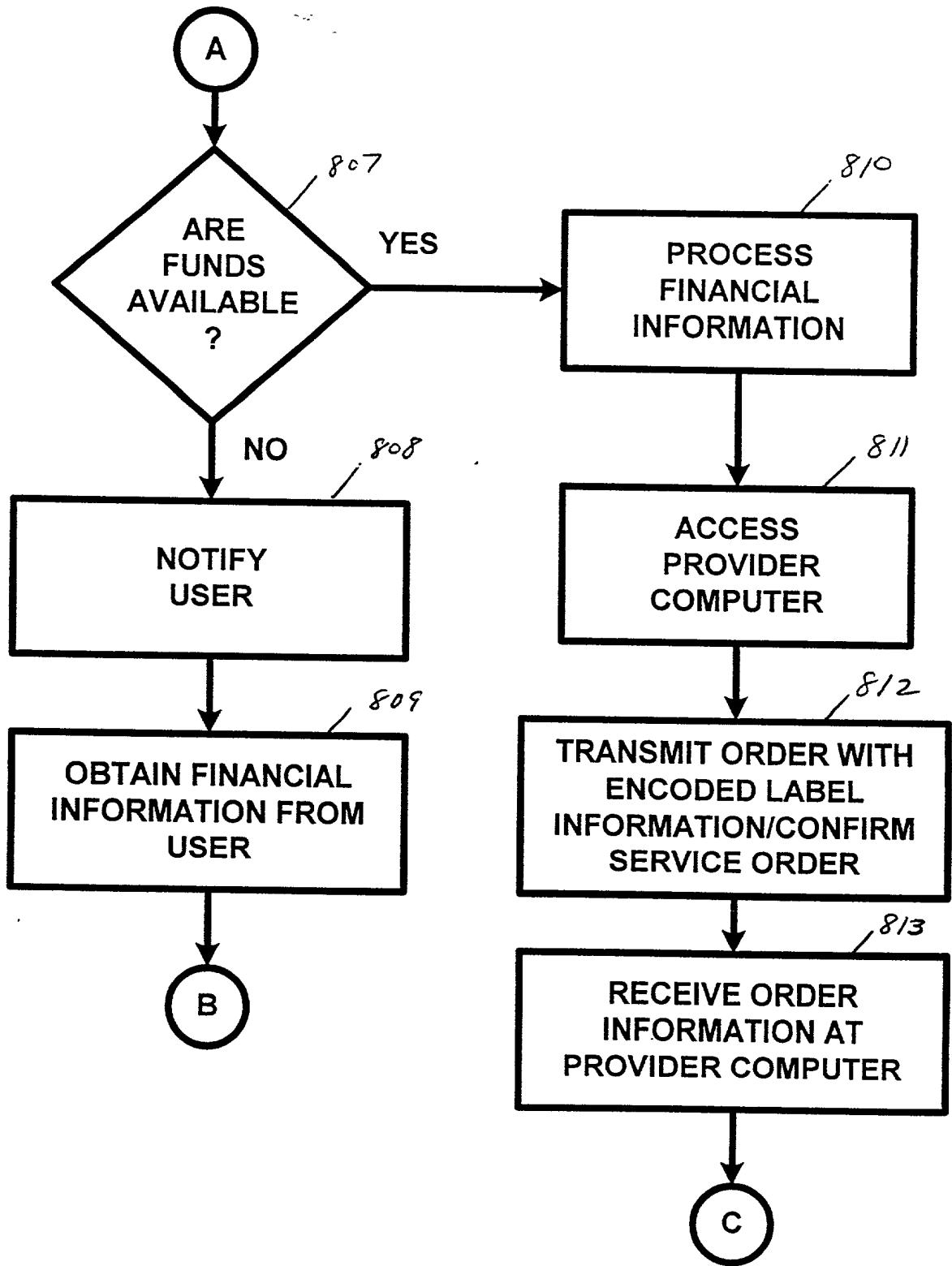


FIG. 8B

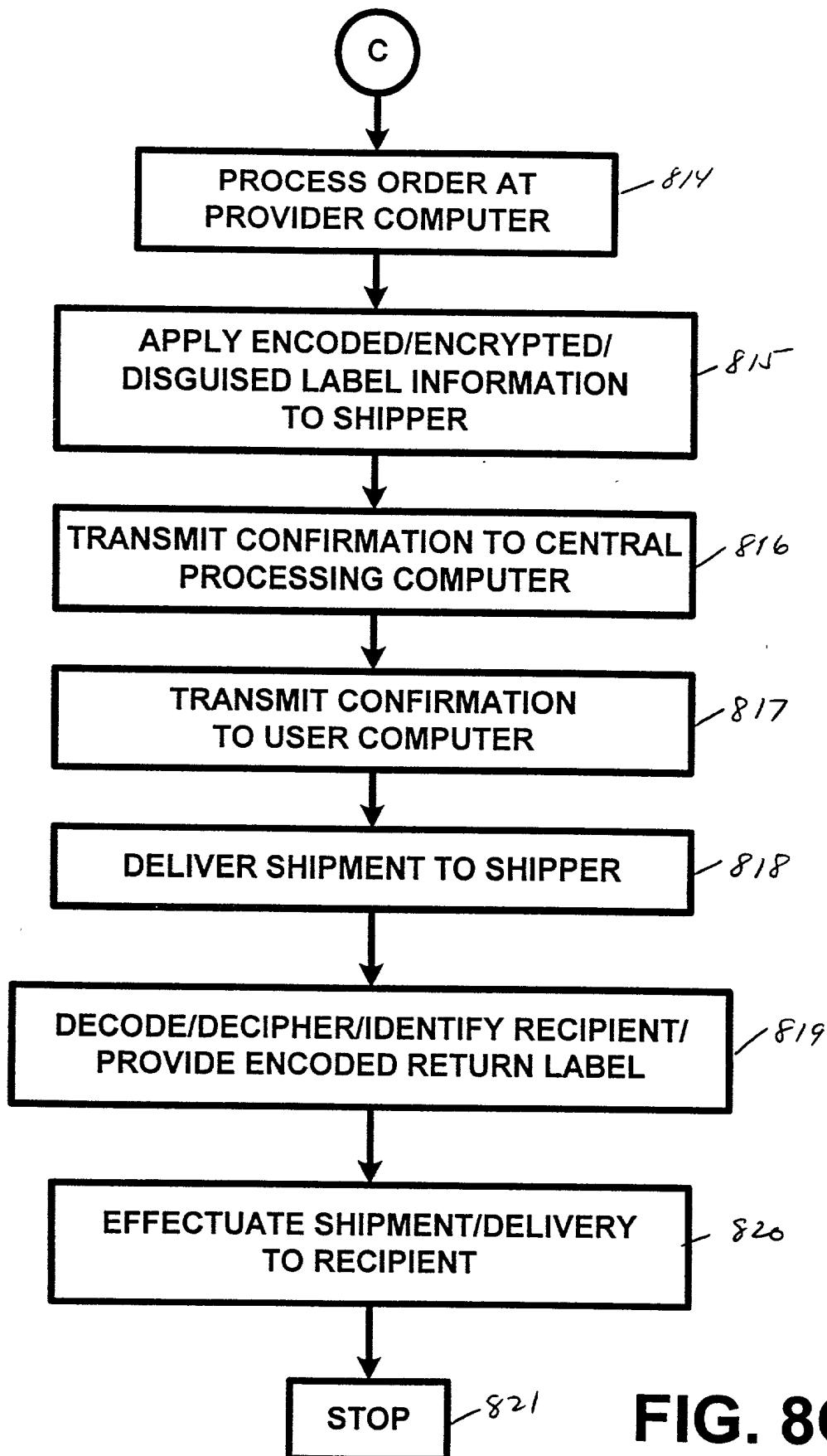


FIG. 8C

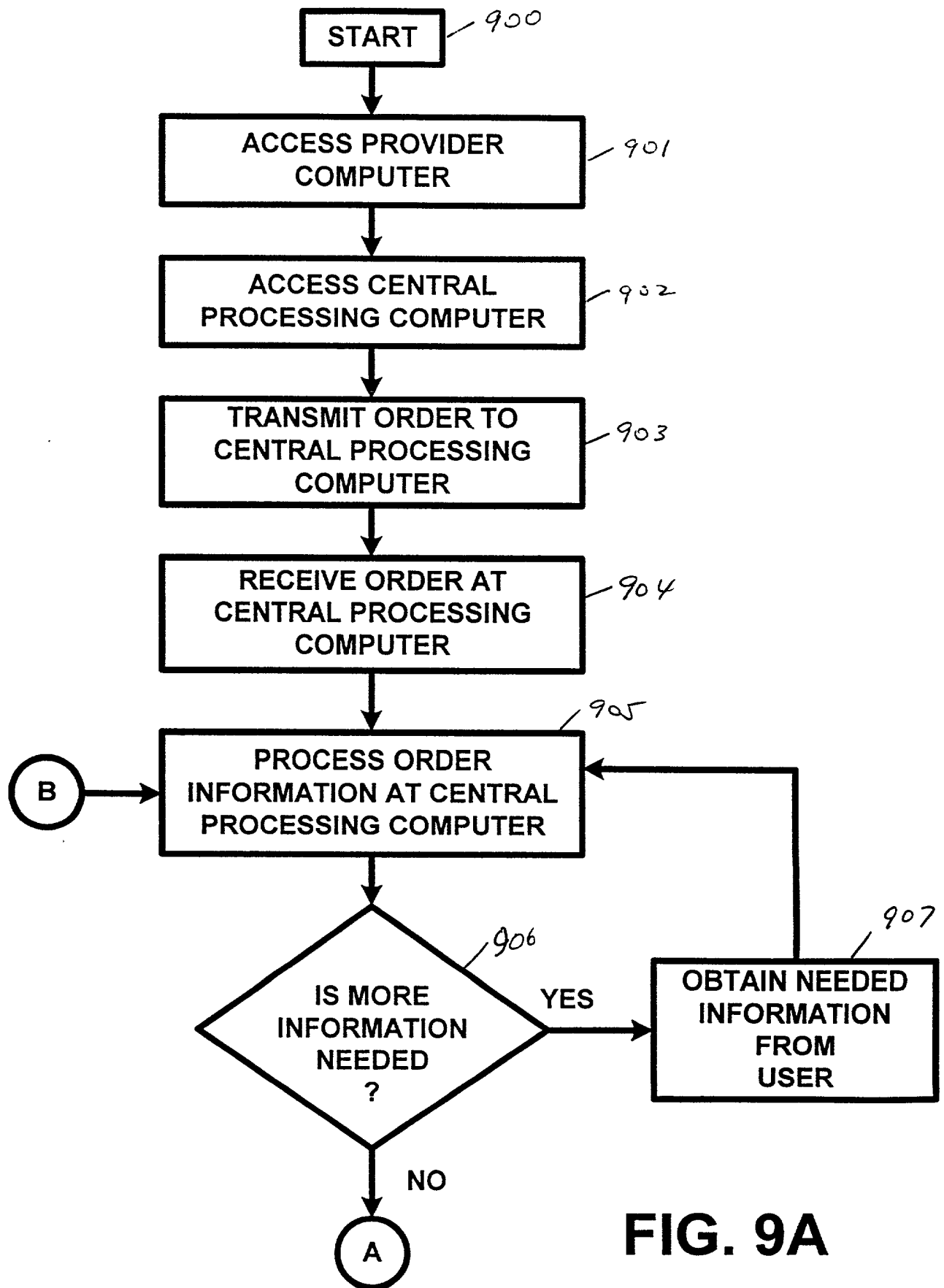


FIG. 9A

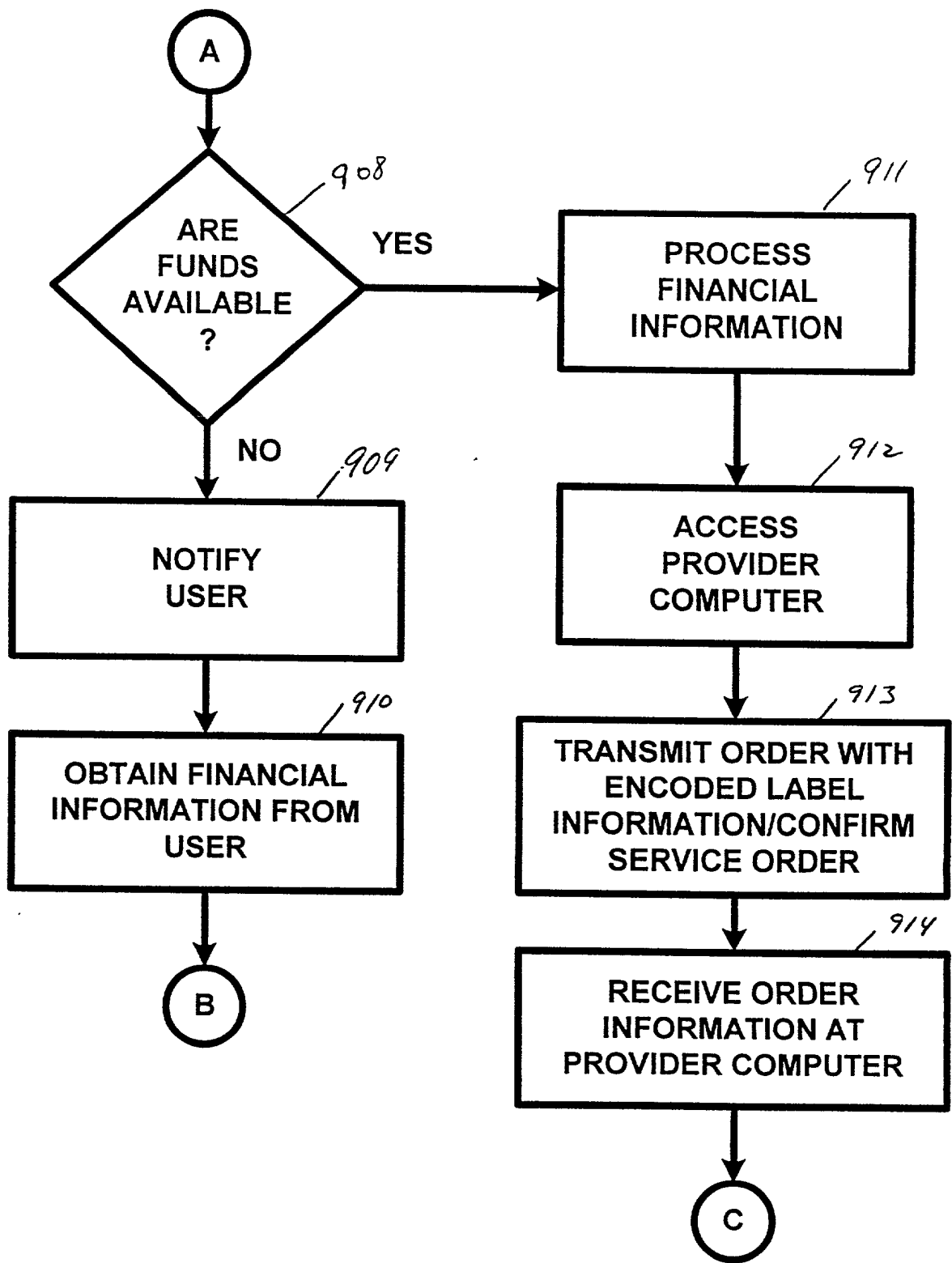


FIG. 9B

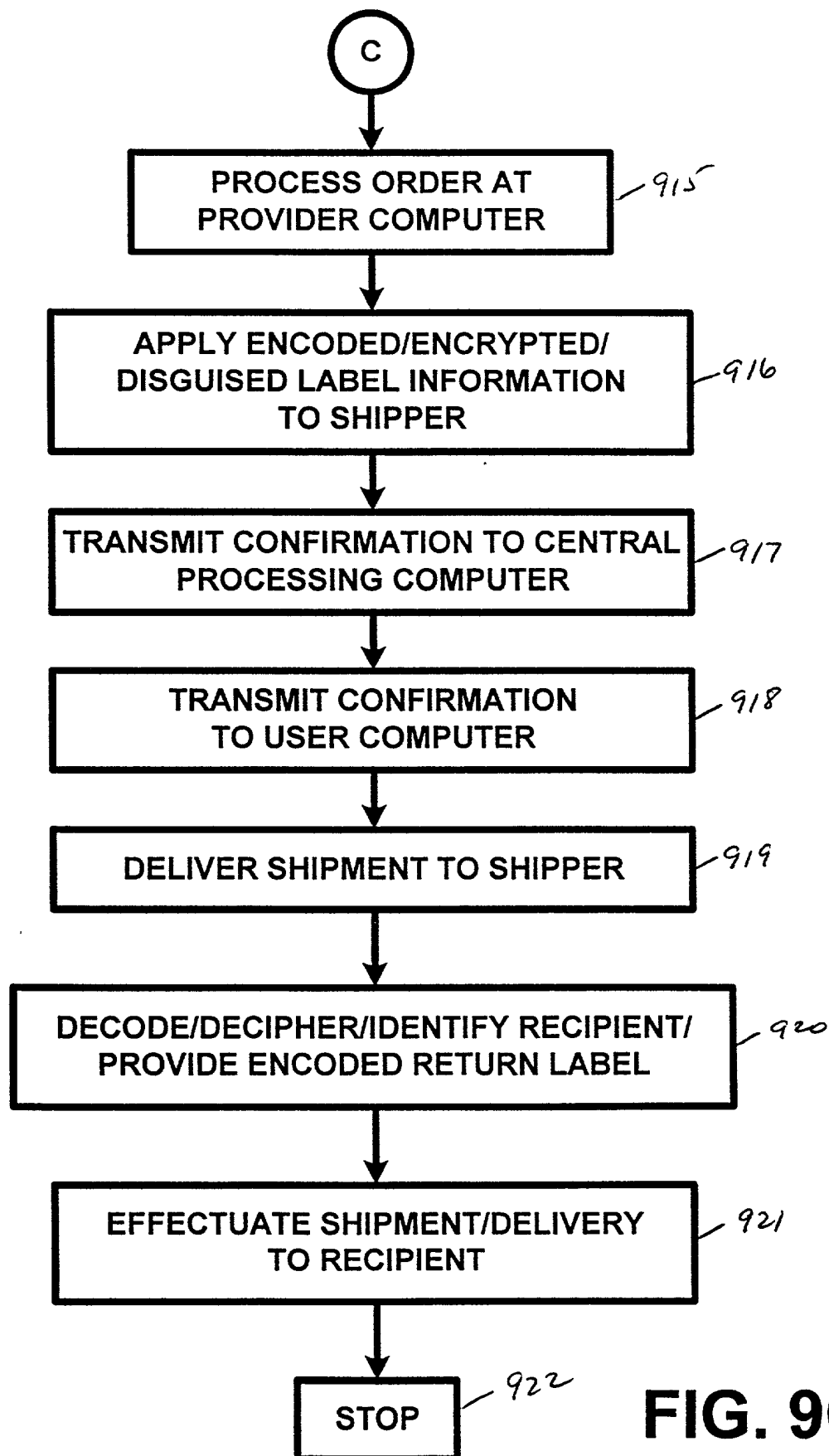


FIG. 9C